

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 11-18-2003  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Method and Apparatus for Timing  
Characterization of Integrated  
Circuit Designs  
Attorney Docket Number:: X-1376 US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 07  
Small Entity?:: No  
Petition included?:: No

### APPLICANT INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: India  
Status:: Full Capacity  
Given Name:: Sudip  
Middle Name:: K.  
Family Name:: Nag  
Street:: 2879 Rubino Circle  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95125

### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

**REPRESENTATIVE INFORMATION**

<b>Representativ Customer Number::</b>	<b>24309</b>	
--	--------------	--

**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124